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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO. CONFIRMATION NO.	
09/474,359	12/29/1999	JEFF C. MORRISS	INTL-0294-US 2154	
7590 12/15/2003		EXAMINER KIM, KEVIN		
TIMOTHY N TROP				
TROP PRUNER HU & MILES PC 8554 KATY FREEWAY STE 100			ART UNIT	PAPER NUMBER
HOUSTON, T	X 77024		2634 20 DATE MAILED: 12/15/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

•		Applicat	tion No.	Applicant(s)	_			
Office Action Summary			359	MORRISS, JEFF C.				
			er	Art Unit				
		Kevin Y	Kim	2634				
Period fo	The MAILING DATE of this communica or Reply	ntion appears on th	ne cover sheet with the	correspondence address				
THE I - External after - If the - If NO - Failur - Any r	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICA nations of time may be available under the provisions of 3 SIX (6) MONTHS from the mailing date of this communication period for reply specified above is less than thirty (30) of period for reply is specified above, the maximum statutive to reply within the set or extended period for reply will eply received by the Office later than three months after and patent term adjustment. See 37 CFR 1.704(b).	ATION. 37 CFR 1.136(a). In no e cation. lays, a reply within the st ory period will apply and , by statute, cause the ap	event, however, may a reply be atutory minimum of thirty (30) d will expire SIX (6) MONTHS fro plication to become ABANDO	timely filed lays will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).				
_	Responsive to communication(s) filed	on 00-20-2003		·				
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3)[_	closed in accordance with the practice							
Dispositi	on of Claims							
4) 🖾	Claim(s) <u>7,13 and 20-27</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)⊠	☑ Claim(s) <u>20-27</u> is/are allowed.							
6)⊠	Claim(s) <u>7 and 13</u> is/are rejected.							
7)	Claim(s) is/are objected to.			·				
8)	Claim(s) are subject to restriction	n and/or election	requirement.					
Applicati	on Papers							
9) 🗌	The specification is objected to by the E	Examiner.						
10)	The drawing(s) filed on is/are: a)□ accepted or b) objected to by the	e Examiner.				
	Applicant may not request that any objection	on to the drawing(s)	be held in abeyance. S	ee 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the			•				
11)	The oath or declaration is objected to be	y the Examiner. N	lote the attached Offic	ce Action or form PTO-152.				
Priority u	ınder 35 U.S.C. §§ 119 and 120							
12)								
Attachment			_					
2) Notic	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO- nation Disclosure Statement(s) (PTO-1449) Pape			ry (PTO-413) Paper No(s) Patent Application (PTO-152)				
S Patent and Tr	ndament Office	`						

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DETAILED ACTION

Response to Arguments

1. In view of the appeal brief filed on September 29, 2003, PROSECUTION IS HEREBY REOPENED. A new ground of rejection is set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

- (1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,
 - (2) request reinstatement of the appeal.

If reinstatement of the appeal is requested, such request must be accompanied by a supplemental appeal brief, but no new amendments, affidavits (37 CFR 1.130, 1.131 or 1.132) or other evidence are permitted. See 37 CFR 1.193(b)(2).

Claim Rejections - 35 USC § 103

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claim 7 and 13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Tamura et al (previously cited) in view of Iwamoto et al (US 6,292,040).

Referring to Fig. 11, Tamura et al discloses a data receiver and method comprising data buffers (541 –543) to latch different data bit signals (DD1, DD2, ---, DDn). Fig.14 shows "a first circuit" (5301) generating a skew-indicating signal between each of the data bit signals and a clock (clk), i.e., a strobe signal, and "a second circuit" (5302) coupled to the first circuit (5301) and the data buffers (541 –543) for regulating the latching of the data bit signals by the buffers

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based on the respective skews. And yet Tamura et al is silent on the characteristics of the control signal indicating the skew between the data and clock signals and fails to teach "a register associated with a different one of the data bit signals" in the first circuit. simply shows a control signal from the phase comparator to the dotted box (5302) representing a delay chain circuit, i.e., the "second circuit." There is no teaching how the control signal is used to modify the delay line for the clock signal. Iwamoto et al disclose a technique to the deficiency by teaching a phase comparator circuit whose output is controlling the variable delay via a register. See Fig. 1 and col.8, lines 5-7. Specifically, the comparator has a shift register indicating/storing the degree of skew based on an up/down control signal. See col. It is noted that the up/down signal takes the form of a pulse train signal whose duty cycle represents the skew since the signal is either high or low depending the relative skew of two input signals' phases. Thus, it would have been obvious to one skilled in the art at the time the invention was made to use a phase comparator that produces a pulse train whose duty cycle representing the skew of two inputs and has a shift register indicating the skew to control the variable line for adjusting the clock/strobe signal in Tamura et al's receiver for the purpose of actually implementing the skew correction circuit of Tamural et al, as taught by Iwamoto.

Allowable Subject Matter

4. Claims 20-27 are allowed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin Y Kim whose telephone number is 703-305-4082. The examiner can normally be reached on 8AM --5PM M-F.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Stephen Chin can be reached on 703-305-4714. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9314.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

kvk

STEPHEN CHIN
SUPERVISORY PATENT EXAMINEI
TECHNOLOGY CENTER 2600